

# PATENT ABSTRACTS OF JAPAN

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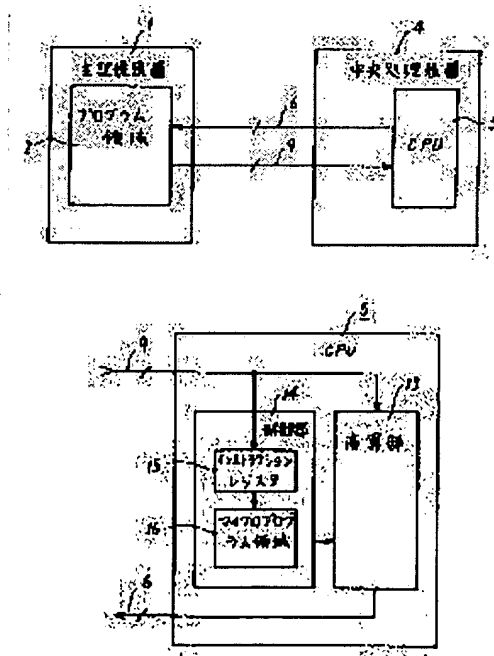
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## (54) DETECTION FOR TROUBLE OF COMPUTER INSTRUCTION CODE

### (57)Abstract:

**PURPOSE:** To detect trouble without adding trouble detecting hardware by setting distances to all instruction codes.

**CONSTITUTION:** When an instruction code is transferred from a main storage device 1, it is latched in an instruction register 15 of a CPU5 from a program area 2 through an input bus 9. The control is jumped to an instruction processing area in accordance with a jump instruction in an instruction code allocation area of a program area 16. At this time, if the instruction code is normal, the control is jumped to an instruction execution area to perform a corresponding processing. If an error occurs in the instruction code, the control is jumped to a wrong instruction processing area to perform an instruction code reloading processing. If several instruction code errors are detected continuously, the error is detected as permanent trouble of the instruction code, namely, the trouble of a main storage device 1.



## LEGAL STATUS

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